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PTO/SB/21 (09-04)

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Total Number of Pages in This Submission

Application Number	10/521,889
Filing Date	May 25, 2005
First Named Inventor	William ROBBINS
Art Unit	2133
Examiner Name	Unassigned
Attorney Docket Number	0903-003

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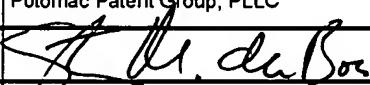
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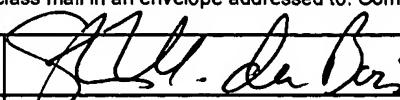
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Firm Name	Potomac Patent Group, PLLC		
Signature			
Printed name	Steven M. duBois		
Date	June 23, 2005	Reg. No.	35,023

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Steven M. duBois

Date June 23, 2005

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Patent
Attorney's Docket No. 0903-003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)
William ROBBINS et al.) Group Art Unit: 2133
Application No.: 10/521,889) Examiner: Unassigned
Filed: May 25, 2005)
For: PROCESSOR ARRAY)
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INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97(b)

Commissioner for Patents
Alexandria, VA 22313-1450

Sir:

Applicants submit herewith PTO/SB/08A and PTO/SB/08B - Information Disclosure Statement together with any required copies of patents, publications or other information of which Applicants are aware, which Applicants believe may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

Copies of the foreign search reports, in which these documents were cited is also attached. It is noted that US 2002/0174318 A1, is an equivalent of GB 2348976 A, which was cited in the Great Britain Search Report dated April 1, 2005.

This Information Disclosure Statement is submitted before the mailing date of a first Office Action on the merits and therefore no fee is required.

It is requested that the information disclosed herein be made of record in this application.

Respectfully submitted,
POTOMAC PATENT GROUP PLLC

By:


Steven M. duBois
Registration No. 35,023

Date: June 23, 2005

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 3

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Application Number	10/521,889
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Attorney Docket Number	0903-003

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FOREIGN PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (If known)				
		WO 01/02960 A1	01/11/2001	CLERMIDY et al.		
		WO 02/50624 A2	06/27/2002	CLAYDON et al.		
		WO 91/11770	08/08/1991	LEGENDI et al.		
		EP 0 180 212 A2	05/07/1986	EATON et al.		

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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NON PATENT LITERATURE DOCUMENTS

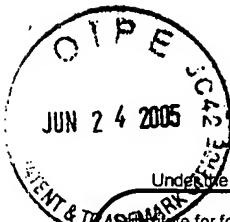
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	1	Popli, S. P., et al., "A Reconfigurable VLSI Array for Reliability and Yield Enhancement," Proceedings of the International Conference on Systolic Arrays, 1988, pp. 631-642.	
	2	John, L. K., et al., "A Dynamically Reconfigurable Interconnect for Array Processors," IEEE Transactions on Very Large Scale Integration (VLSI) Systems, Vol. 6, No. 1,	
		March 1998, pp. 150-157.	
	3	Shigei, N., et al., "On Efficient Spare Arrangements and an Algorithm with Relocating Spares for Reconfiguring Processor Arrays," IEICE Transactions on Fundamentals of	
		Electronics, Communications and Computer Sciences, Vol. E80-A, No. 6, June 1997, pp. 988-995.	
	4	Schmidt, U., et al., "Datawave: A Single-Chip Multiprocessor for Video Applications," IEEE Micro, Vol. 11, No. 3, June 1991, pp. 22-25, 88-94.	
	5	Chean, M., et al., "A Taxonomy of Reconfiguration Techniques for Fault-Tolerant Processor Arrays," Computer, IEEE Computer Society, Vol. 23, No. 1, January 1990, pp. 55-69.	
	6	Kamiura, N., et al., "A Repairable and Diagnosable Cellular Array on Multiple-Valued Logic," Proceedings of the 23rd International Symposium on Multiple-Valued Logic, 1993,	
		pp. 92-97.	
	7	LaForge, L., "Extremely Fault Tolerant Arrays," Proceedings: International Conference on Wafer Scale Integration, 1989, pp. 365-378.	

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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**INFORMATION DISCLOSURE
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Sheet 3

of 3

Application Number	10/521,889
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First Named Inventor	William ROBBINS
Art Unit	2133
Examiner Name	Unassigned
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	8	International Search Report of PCT/GB03/02772, dated November 30, 2004.	
	9	Great Britain Search Report of Application No. GB 0216880.5, dated January 28, 2003.	
	10	Great Britain Search Report of Application No. GB 0216880.5, dated October 9, 2003.	
	11	Great Britain Search Report of Application No. GB 0216880.5, dated April 1, 2005.	

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